NNIPRE816ADCIOT

80µW 200MSps IoT Reconfigurable ADC

NNIP

Features

- Implements Reconfigurable ADC core from 8-bit to 16-bit for sensors applications
- 80µW maximum power consumption
- Maximum sampling rate of 200MSps
- Handles multiple sensors at the same time through multiplexing with automatic reconfiguration/tunability
- Supports high-performance and low power operating modes.
- Optimized for multiplexing and sensor fusion applications

Applications

- Low power tiny area Tunable ADC (8-bit, 10-bit, 12-bit, and 16-bit) for biomedical, body area network, wearable, and sensor fusion applications.

Description

The NNIPRE816ADCIOT AMS IP is intended to be used in as a hard ASIC IP which can be cost-effectively ported across process nodes and technology foundries.

The hard IP provides up to 200MSps sampling frequency with maximum power consumption of 80µW and very small Silicon area.

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IP Deliverables

- Datasheet
- Layout View (gds2) for the hard IP ADC core
- Integration Support

Najah Now IP

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NajahNow Digital hard IP cores have been silicon verified in a number of foundries (TSMC, Global Foundries, UMC, and Fujitsu) at nodes ranging from 180nm to 18nm.

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